

User Interface Changes

for HP 16600A/16700A-series logic analysis systems - Version A.01.40.00 and higher

This covers:

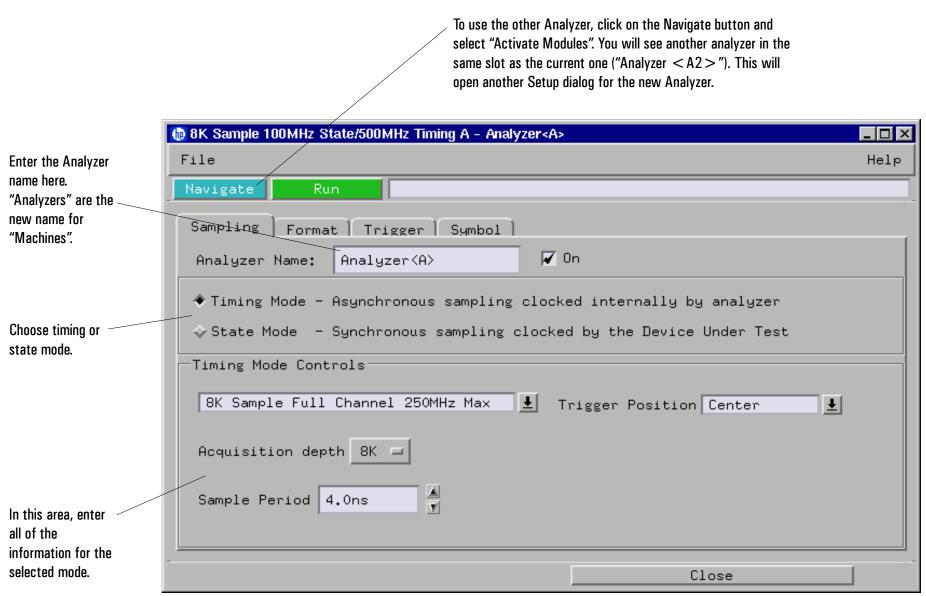
- Sampling Tab
- Setting Up State Mode Clocks
- Format Tab
- Trigger Tab

Hewlett-Packard Company Printed in the USA

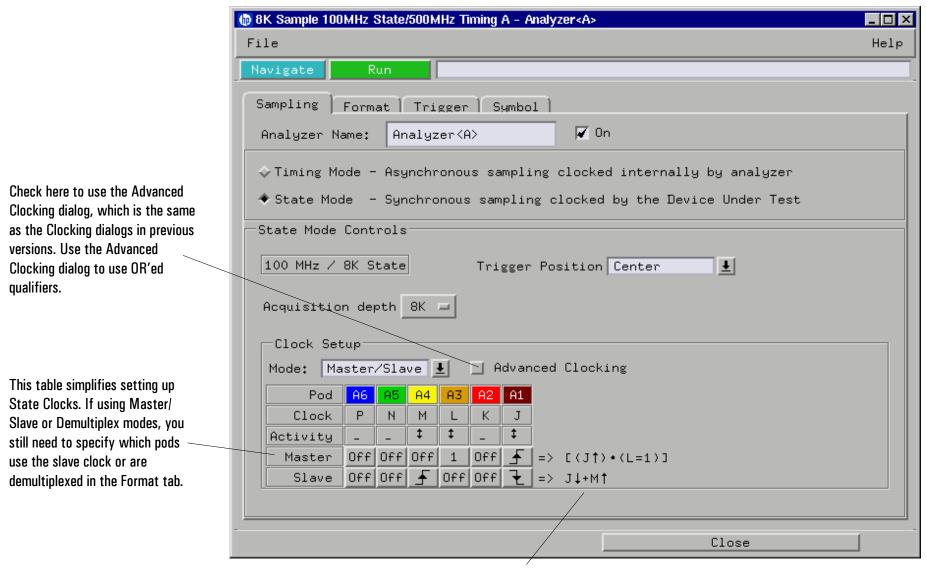
Manual Part Number B3760-92002, December 1999



Sampling Tab



Setting Up State Mode Clocks

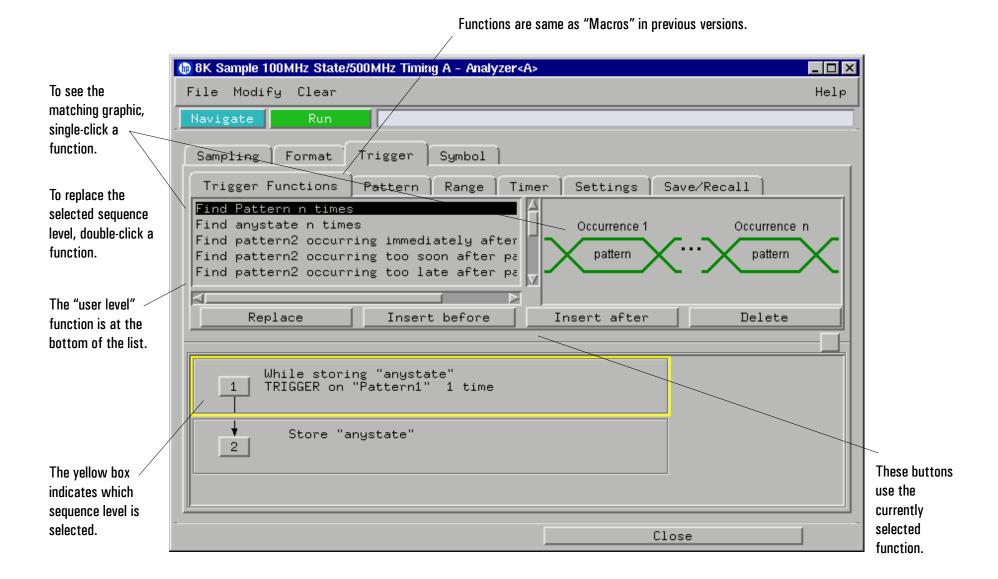


The clock equations reflect the selected clocks.

Format Tab

Click here for the Pod Assignment dialog which is the Select Master, Slave or Demultiplex here. It's not enough to specify the state clocks in the Sampling Tab. same as the Config Tab in previous versions. ₱ 8K Sample 100MHz State/500MHz Timing A - Analyzer<A> _ 🗆 × File Edit Help Navigate Format Trigger Sampling Symbol Data On Clocks Pod Pod A2 🗖 Slave A2 Slot A Assignment... TTL Setup/Hold... Demultiplex = ŔŔ 15 87 15 87 + Label1 Apply Close

Trigger Tab



Summary

- "Machines" have become "Analyzers". The default names of the analyzers are "Analyzer <Slot>" and "Analyzer <Slot2>". For example, if a module is in Slot A, then the analyzers are "Analyzer <A>" and "Analyzer <A>".
- To activate a second analyzer, click on the navigate button, and choose "Activate".
- The new Sampling tab contains all information related to when the logic analyzer will take a sample, including the selection of Timing or State Modes.
- The Config Tab has become the Pod Assignment dialog. It is accessed via a button in the Format tab.
- Trigger "macros" have become trigger "functions".
- Click on a trigger function in the list; then, click on the "Replace", "Insert Before", or "Insert After" buttons. Double-clicking on a function is a short cut for "Replace".
- The "User Level" function is at the bottom of the list.